

Fig. 1

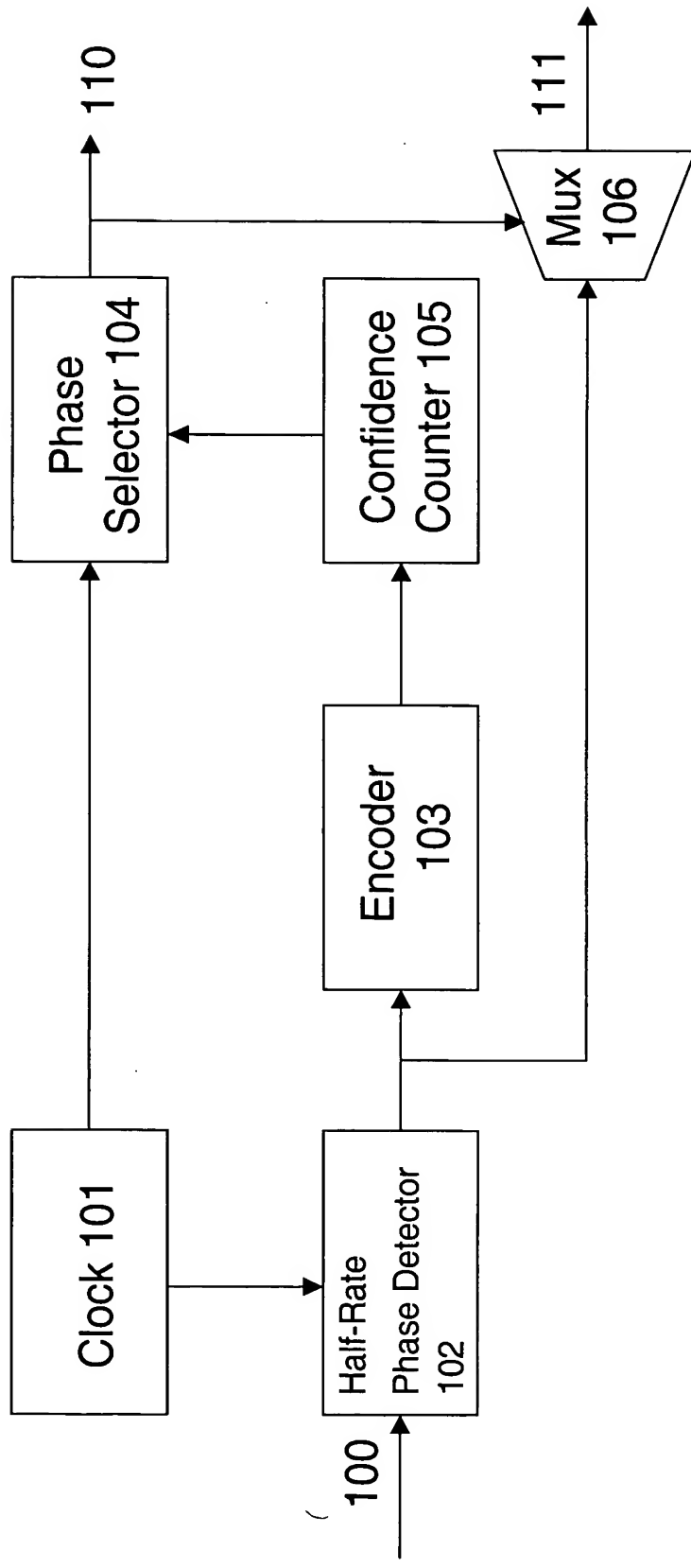


Fig. 3A

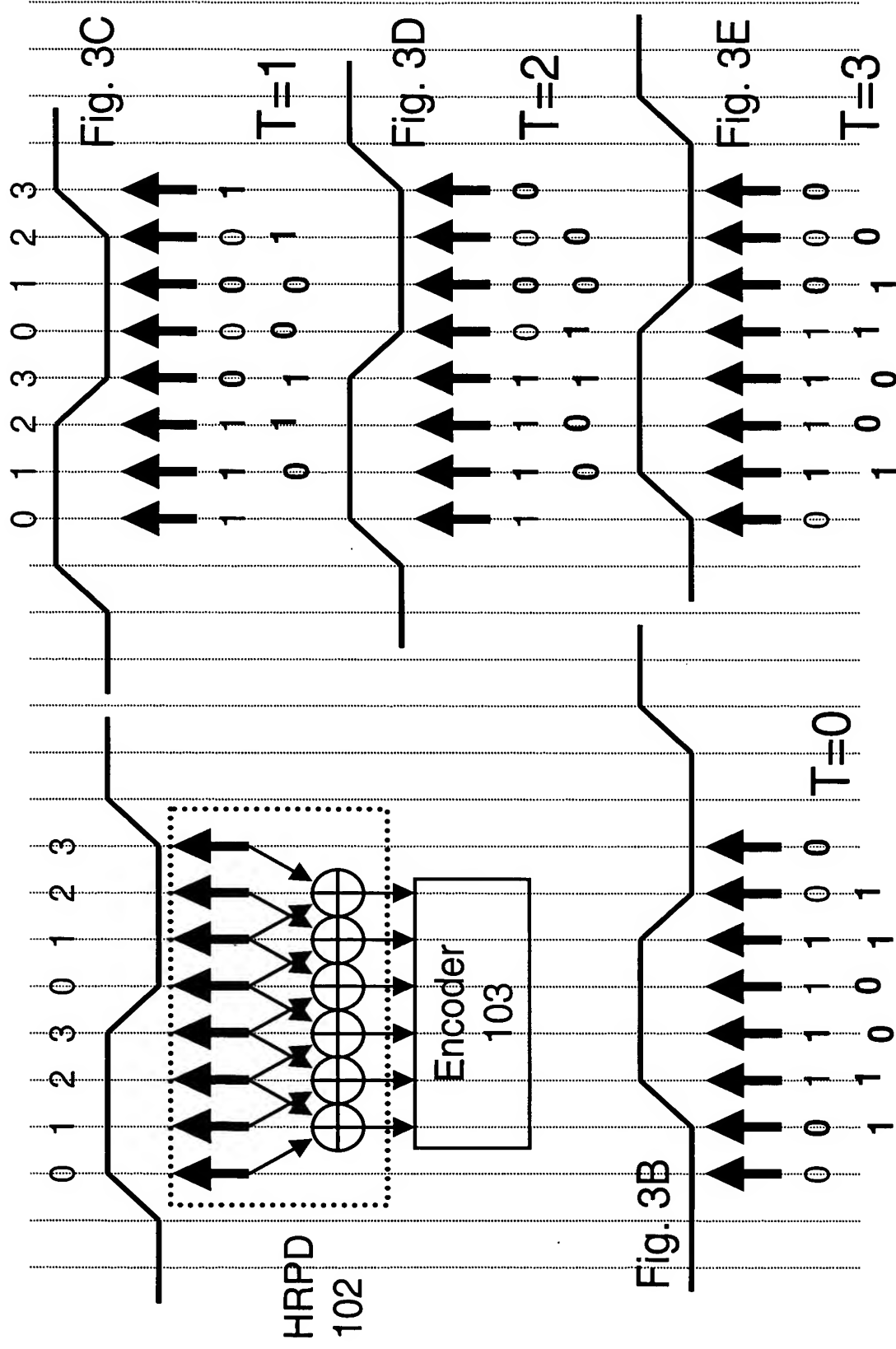


Fig. 4A

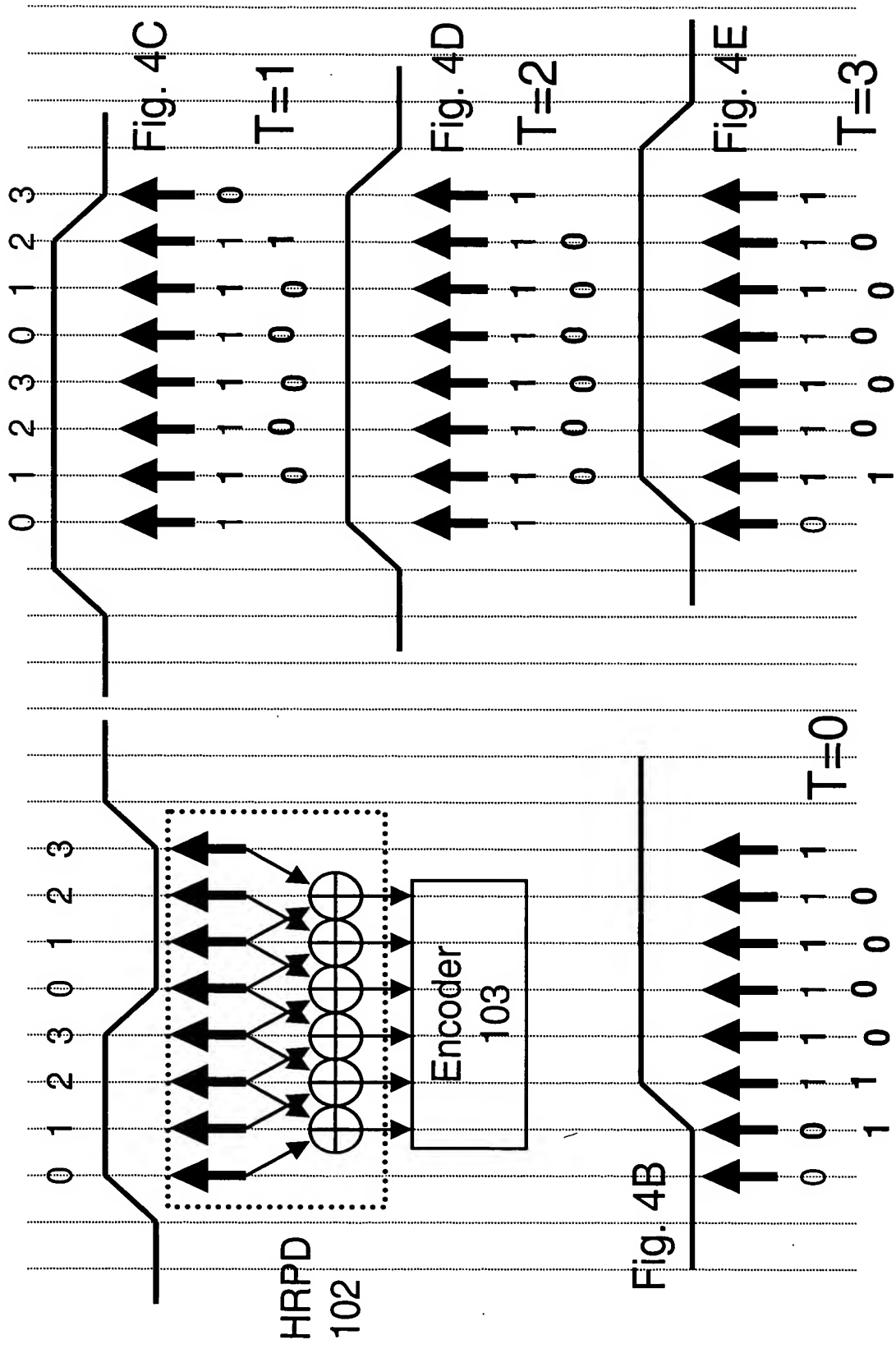


Fig. 5A

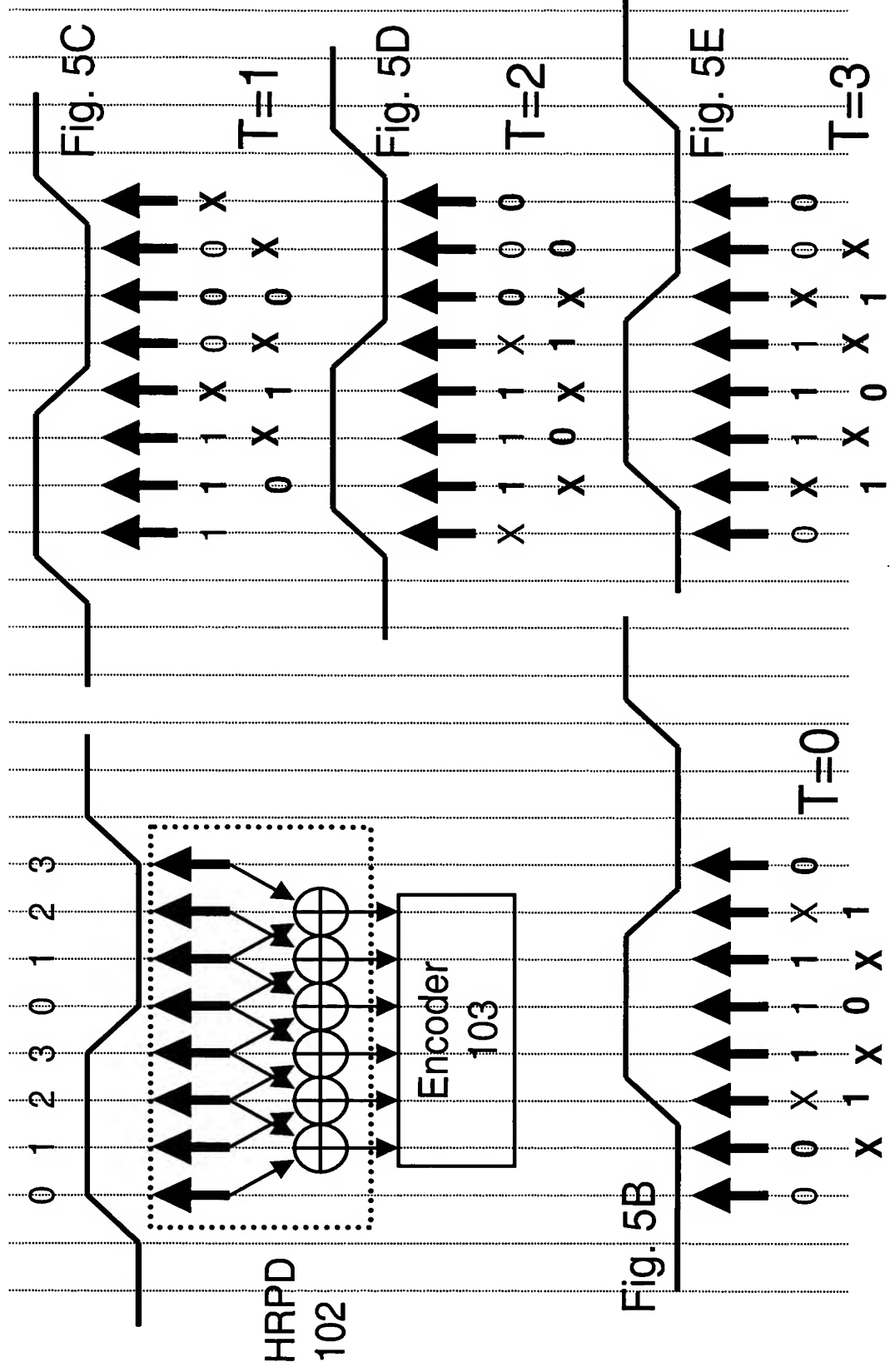


Fig. 6A

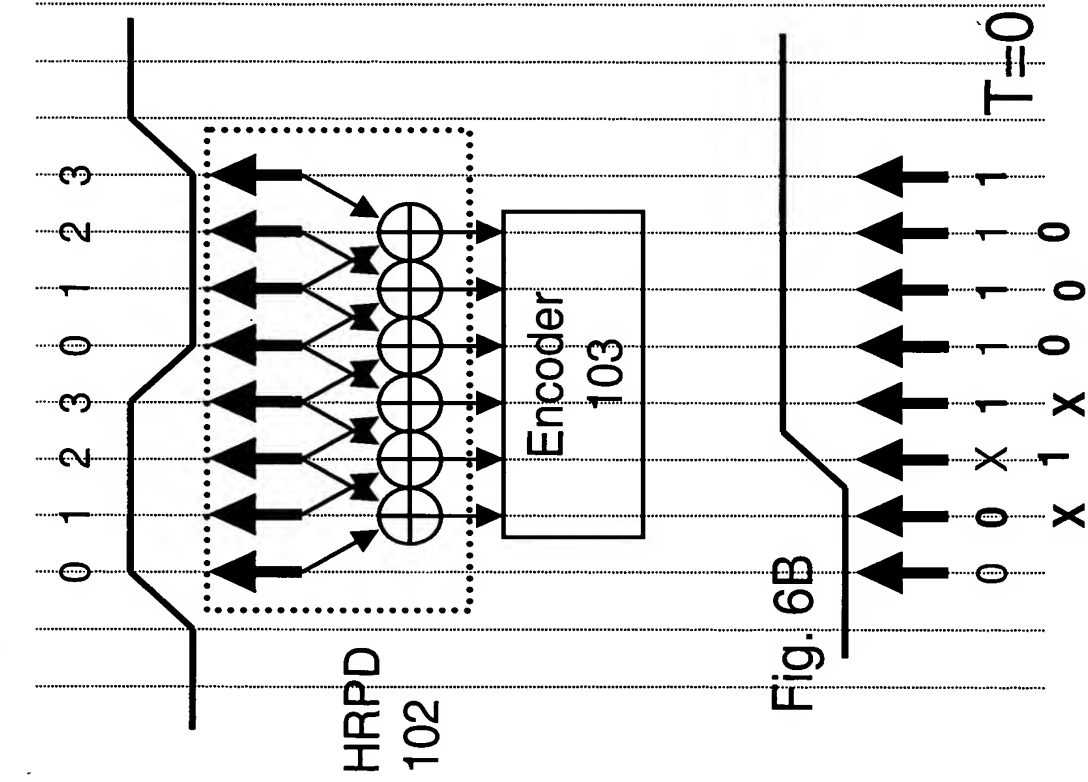


Fig. 6B

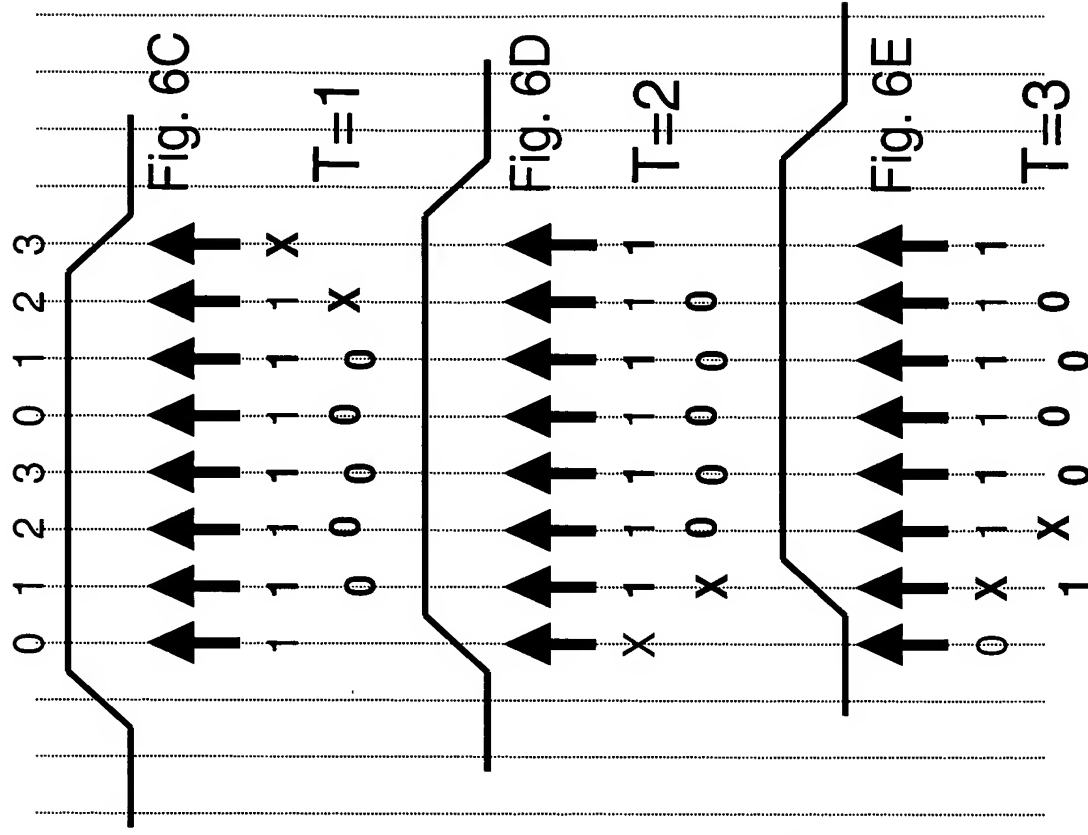
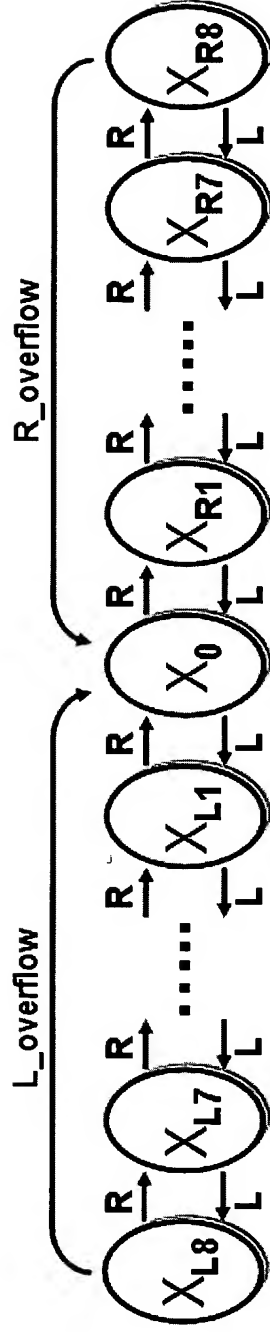


Fig. 7

➤ State Diagram

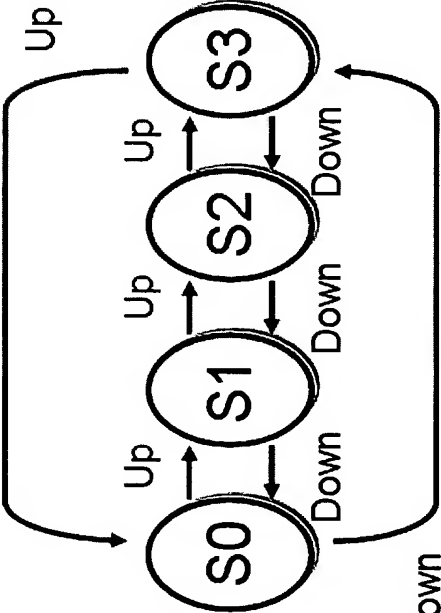


State	X_8	X_7	X_6	X_5	X_0	X_1	X_2	X_3	X_4
Initial State	0	0	0	0	1	0	0	0	0

Fig. 8

Up/Down Phase Selector

➤ State Diagram



Initial State $S_{ini}=S_0$

System State	S_0	S_1	S_2	S_3
Optimum Phase	T_0	T_1	T_2	T_3
Sampling Phases	P_0, P_4	P_1, P_5	P_2, P_6	P_3, P_7
O_Sel	0	1	0	1

Fig. 9

